

CANTURK ISCI

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RESEARCH

- Power aware computer architecture
- Microprocessor power and thermal modeling and measurement
- Power and performance based phase behavior characterization, detection and prediction for applications under real system effects

EDUCATION

- | | |
|--------------|---|
| 2001-present | <i>Princeton University, Princeton, NJ</i>
Ph.D. Electrical Engineering
• Advised by Prof Margaret Martonosi |
| 2000 - 2001 | <i>University of Westminster, London, UK</i>
M.Sc. VLSI System Design
• Thesis advised by Prof Izzet Kale & Prof R.C.S. Morling |
| 1996 - 2000 | <i>Bilkent University, Ankara, Turkey</i>
B.S. Electrical and Electronics Engineering |

EXPERIENCE

- | | |
|--------------------|--|
| 2001-present | <i>Princeton University, Department of Electrical Engineering</i>
Research Assistant in Parapet Research Group |
| July-December 2004 | <i>IBM T.J. Watson Research Center, Yorktown Heights, New York</i>
Coop in Reliability and Power Aware Microarchitecture Group
• Manager: Pradip Bose & Mentor: Alper Buyuktosunoglu |
| Summer 1999 | <i>ASELSAN Electronics, Ankara, Turkey</i>
Intern in Electronic Design Department |
| Summer 1999 | <i>Bilkent University, Electronics Engineering Department</i>
Intern in DSP programming for wireless communication project |

HONORS

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|-------------|---|
| 2001 - 2002 | <i>Graduate Fellowship</i> , Department of Electrical Engineering, Princeton University |
| 2001 | <i>M.Sc. with Distinction</i> , University of Westminster, Department of Electronic Systems |
| 2000 - 2001 | <i>Millennium Scholarship</i> , British Council |
| 1996 - 2000 | <i>Undergraduate Fellowship</i> , Bilkent University |

PUBLICATIONS

Canturk. Isci and Margaret Martonosi. Identifying program power phase behavior using power vectors. In *Proceedings of the 6th IEEE International Workshop on Workload Characterization (WWC-6)*, November 2003.

Canturk Isci and Margaret Martonosi. Runtime Power Monitoring in High-End Processors: Methodology and Empirical Data. In *Proceedings of the 36th ACM/IEEE International Symposium on Microarchitecture*, December 2003.