CANTURK ISCI

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RESEARCH

- Power aware computer architecture, microprocessor power and thermal modeling and measurement
- Application power/performance phase behavior characterization, detection and prediction.

EDUCATION

2001-present	*Princeton University, Princeton, NJ* Ph.D. Electrical Engineering • Advised by Prof Margaret Martonosi
2000 - 2001	*University of Westminster, London, UK* M.Sc. VLSI System Design • Thesis advised by Prof Izzet Kale & Prof R.C.S. Morling
1996 - 2000	*Bilkent University, Ankara, Turkey* B.S. Electrical and Electronics Engineering

EXPERIENCE

2001-present	*Princeton University, Department of Electrical Engineering* Research Assistant in Parapet Research Group
July-December 2004	*IBM T.J. Watson Research Center, Yorktown Heights, New York* Coop in Reliability and Power Aware Microarchitecture Group • Manager: Pradip Bose & Mentor: Alper Buyuktosunoglu
Summer 1999	*ASELSAN Electronics, Ankara, Turkey* Intern in Electronic Design Department
Summer 1999	*Bilkent University, Electronics Engineering Department* Intern in DSP programming for wireless communication project

HONORS

2001 - 2002	*Graduate Fellowship*, Department of Electrical Engineering, Princeton University
2001	*M.Sc. with Distinction,* University of Westminster, Department of Electronic Systems
2000 - 2001	*Millennium Scholarship*, British Council
1996 - 2000	*Undergraduate Fellowship*, Bilkent University

PUBLICATIONS

Canturk Isci, Zhigang Hu, Margaret Martonosi and Pradip Bose. Building Microarchitectural Stressmarks for Thermal Testing. In *Austin Conference on Energy-Efficient Design (ACEED-2005) – Internal Session*, March 2005.

Canturk Isci, Margaret Martonosi and Alper Buyuktosunoglu. Workload Phase Duration Prediction and its Application to DVS. In *Austin Conference on Energy-Efficient Design (ACEED-2005) – Internal Session*, March 2005.

Canturk Isci, Gilberto Contreras and Margaret Martonosi. Building Hardware Performance Counters for Detailed Runtime Power and Thermal Estimations: Experiences and Proposals. In *Workshop on Hardware Performance Monitor Design and Functionality (in HPCA-11)*, February 2005.

Canturk Isci and Margaret Martonosi. Runtime Power Monitoring in High-End Processors: Methodology and Empirical Data. In *Proceedings of the 36th ACM/IEEE International Symposium on Microarchitecture*, December 2003.

Canturk. Isci and Margaret Martonosi. Identifying program power phase behavior using power vectors. In *Proceedings of the 6th IEEE International Workshop on Workload Characterization (WWC-6)*, November 2003.